

**IN THE SPECIFICATION:**

*Please insert the following new paragraph after the Title and before the "TECHNICAL FIELD":*

**-- RELATED APPLICATIONS**

This application is the U.S. National Phase under 35 U.S.C. § 371 of International Application No. PCT/JP2005/007073, filed on April 12, 2005, which in turn claims the benefit of Japanese Application No. 2004-120168, filed on April 15, 2004, the disclosures of which Applications are incorporated by reference herein. --

*Please amend the paragraph beginning on page 3 at line 13 as follows:*

[0009] In order to achieve the object, the present invention of ~~claim 1~~ relates to a semiconductor integrated circuit, to which a power supply voltage is supplied via electromagnetic waves received by an antenna coil, and which exchanges data via the antenna coil, the semiconductor integrated circuit comprising:

a memory circuit storing data;

a pair of terminals to which the antenna coil is connected;

a modulating-transmitting circuit for transmitting data via the antenna coil by varying a load between the terminals according to data to be transmitted;

a transmission control circuit for outputting data stored in the memory circuit to the modulating-transmitting circuit; and

a reset signal generating circuit for outputting a reset signal for resetting the transmission control circuit to the transmission control circuit when the power supply voltage decreases below a predetermined threshold value, wherein:

the transmission control circuit is configured to output a transmission status signal, which indicates that data is being transmitted; and

the reset signal generating circuit is configured so that the threshold value is lower during a period in which the transmission status signal is being output than during a period in which the transmission status signal is not being output.

*Please amend the paragraph beginning on page 4 at line 13 as follows:*

[0011] The present invention ~~of claim 2 is the~~ also relates to a semiconductor integrated circuit ~~of claim 1~~, wherein the reset signal generating circuit includes a voltage-dividing resistor for dividing the power supply voltage and is configured so as to output the reset signal when a voltage obtained by the voltage division by the voltage-dividing resistor is lower than a predetermined reference voltage.

*Please amend the paragraph beginning on page 4 at line 18 as follows:*

[0012] The present invention ~~of claim 3 is the~~ also relates to a semiconductor integrated circuit ~~of claim 2~~, wherein the reset signal generating circuit is configured to change a voltage division ratio so that the voltage obtained by the voltage division is higher during a period in which the transmission status signal is being output than during a period in which the transmission status signal is not being output.

*Please amend the paragraph beginning on page 4 at line 23 as follows:*

[0013] The present invention ~~of claim 4 is the~~ also relates to a semiconductor integrated circuit ~~of claim 3~~, wherein:

the voltage-dividing resistor is a series of resistors including three or more resistors; and

the reset signal generating circuit is configured to change the voltage division ratio by changing the number of transistors of the series of resistors for dividing the power supply voltage.

*Please amend the paragraph beginning on page 5 at line 7 as follows:*

[0015] The present invention ~~of claim 5 is the~~ also relates to a semiconductor integrated circuit ~~of any one of claims 1 to 4~~, wherein the transmission control circuit is configured to output the transmission status signal while simultaneously outputting data stored in the memory circuit to the modulating-transmitting circuit.

*Please amend the paragraph beginning on page 5 at line 13 as follows:*

[0017] The present invention ~~of claim 6 is the~~ also relates to a semiconductor integrated circuit ~~of claim 4~~, wherein the transmission control circuit includes a buffer for holding data stored in the memory circuit and is configured to output data held in the buffer to the modulating-transmitting circuit.

*Please amend the paragraph beginning on page 5 at line 25 and bridging page 6 as follows:*

[0019] The present invention ~~of claim 7 is the~~ also relates to a semiconductor integrated circuit ~~of claim 4~~, wherein the transmission control circuit is configured to stop outputting the transmission status signal after an amount of time greater than or equal to an amount of time required for outputting data to the modulating-transmitting circuit has elapsed since completion of the data output to the modulating-transmitting circuit.

*Please amend the paragraph beginning on page 6 at line 7 as follows:*

[0021] The present invention ~~of claim 8 is the~~ also relates to a semiconductor integrated circuit ~~of claim 4~~, further comprising a high-voltage side reset signal generating circuit for outputting, to the transmission control circuit, a reset signal for resetting the transmission control circuit when the power supply voltage exceeds a predetermined threshold value,

wherein the high-voltage side reset signal generating circuit is configured so that the threshold value is lower during a period in which the transmission status signal is being output than during a period in which the transmission status signal is not being output.

*Please amend the paragraph beginning on page 6 at line 15 as follows:*

[0022] The present invention ~~of claim 9 is the~~ also relates to a semiconductor integrated circuit ~~of claim 8~~, wherein the high-voltage side reset signal generating circuit includes a voltage-dividing resistor for dividing the power supply voltage and is configured to output the reset signal when a voltage obtained by the voltage division by the voltage-dividing resistor is higher than a predetermined reference voltage.

*Please amend the paragraph beginning on page 6 at line 20 as follows:*

[0023] The present invention of ~~claim 10 is the~~ also relates to a semiconductor integrated circuit of ~~claim 9~~, wherein the high-voltage side reset signal generating circuit is configured to change a voltage division ratio so that the voltage obtained by the voltage division during a period in which the transmission status signal is being output is higher than that during a period in which the transmission status signal is not being output.

*Please amend the paragraph beginning on page 6 at line 25 as follows:*

[0024] The present invention of ~~claim 11 is the~~ also relates to a semiconductor integrated circuit of ~~claim 10~~:

the voltage-dividing resistor is a series of resistors including three or more resistors; and

the high-voltage side reset signal generating circuit is configured to change the voltage division ratio by changing the number of transistors of the series of resistors for dividing the power supply voltage.

*Please amend the paragraph beginning on page 7 at line 16 as follows:*

[0026] The present invention of ~~claim 12 is~~ also relates to a contactless type information system, comprising:

a contactless type information medium including the semiconductor integrated circuit of ~~claim 1~~ noted above and an antenna coil connected to the semiconductor integrated circuit for transmitting/receiving electromagnetic waves; and

a data transmitting/receiving device for supplying a power supply voltage to, and exchanging data with, the contactless type information medium via electromagnetic waves.